ctitioner=s Docket No. (03DV09036)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Alan J. Janicek, et al.

Serial No.: 09/682, 765

Filed: October 16, 2001

Art Unit: N/A

Examiner: N/A

For: QUICK-CONNECT POSITIVE TEMPERATURE COEFFICIENT OF RESISTANCE RESISTOR/OVERLOAD ASSEMBLY AND METHOD

**Assistant Commissioner for Patents** Washington, D.C. 20231

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington,

ASSOCIATE POWER OF ATTORNEY
Pursuant to 37 C.F.R. '1.34

Please recognize as Associate Practitioner in the above-identified application. W Carr

W Carr

Respectfully submitted,

GENERAL ELECTRIC COMPANY

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Date: 2-15-2002

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